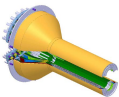
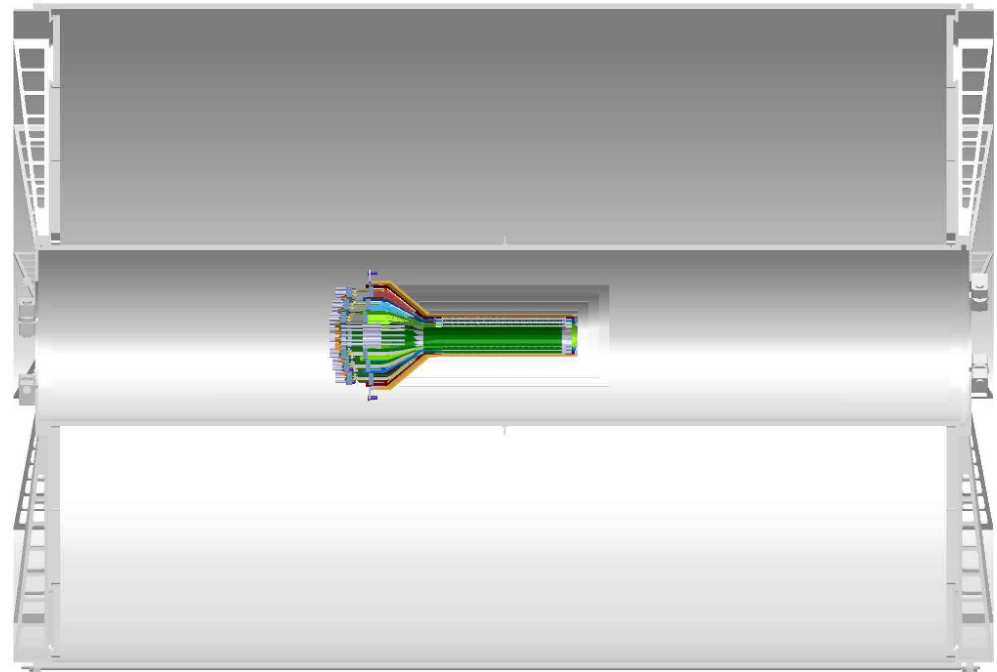
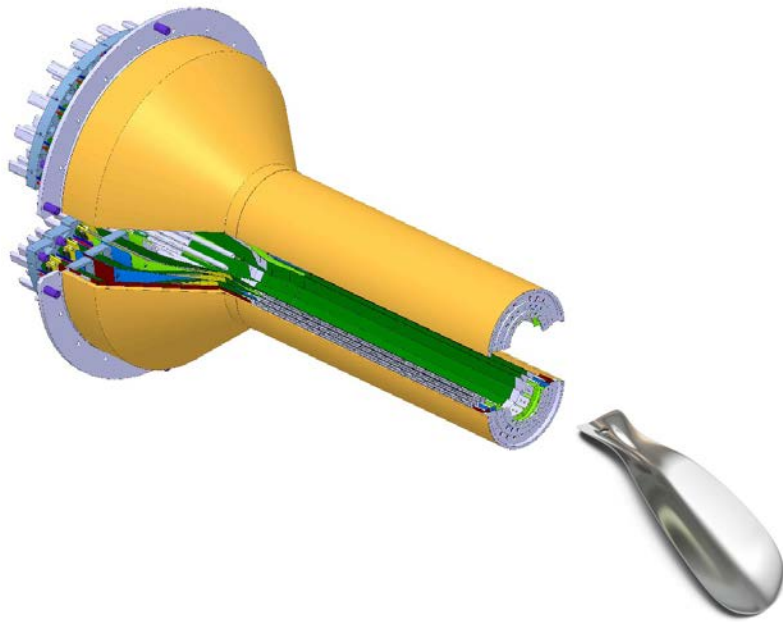


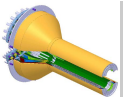
MVTX/MAPS Integration



Problem: Shoehorning MAPS into sPHENIX

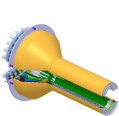


Images courtesy of Walt Sondheim

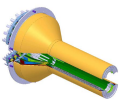
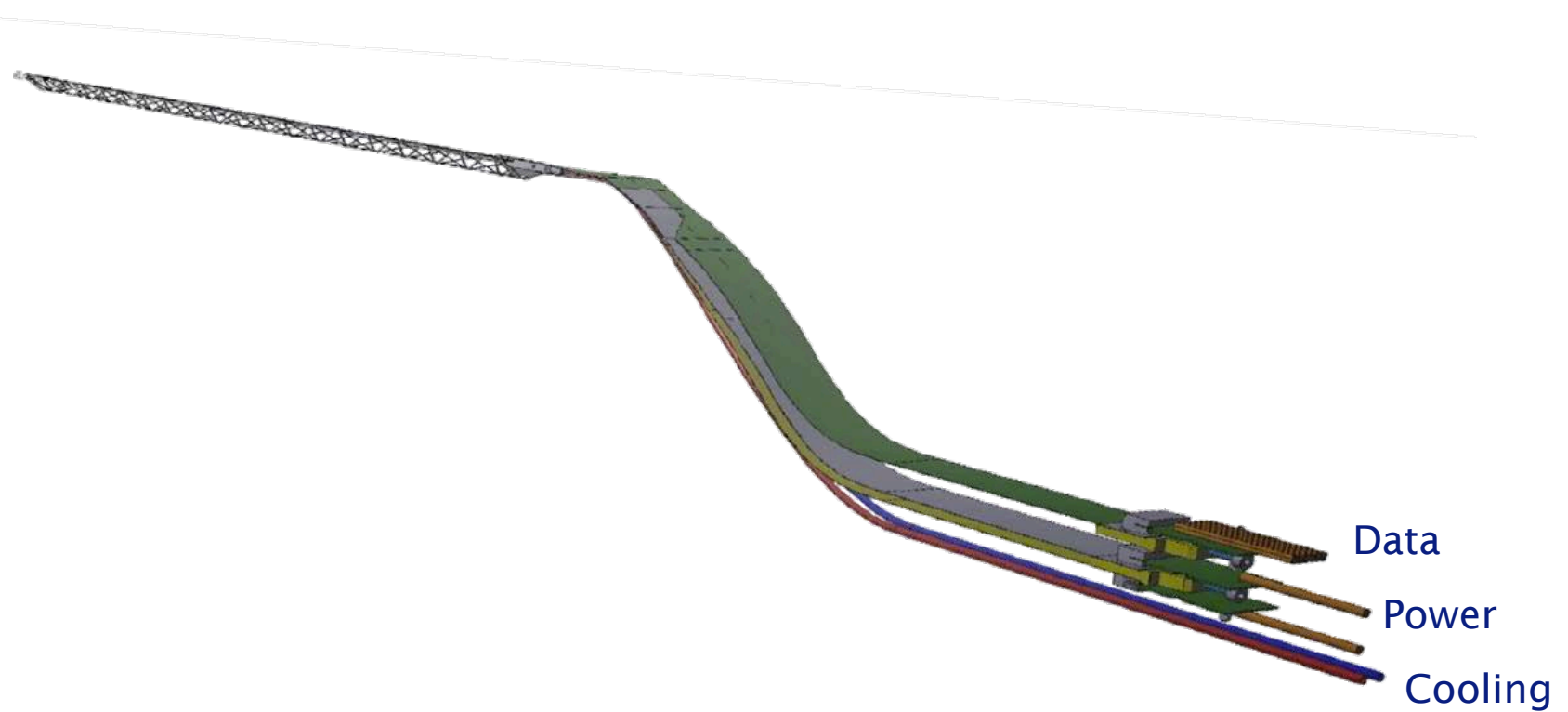


Current Information

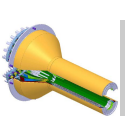
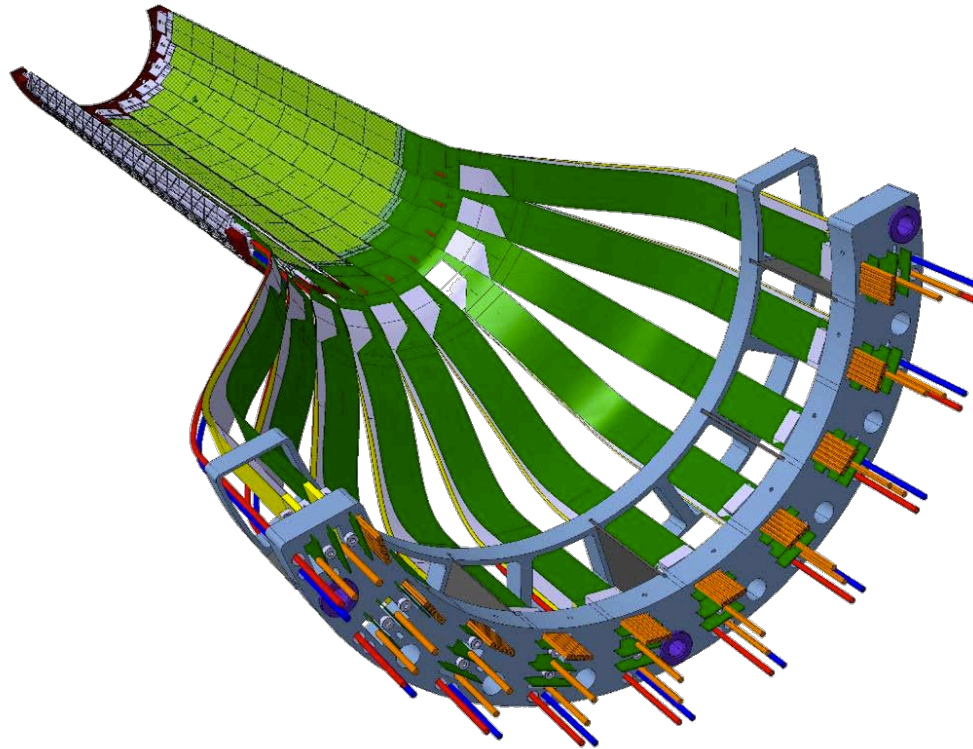
- Apology: I collected a huge brain dump of information, not yet all digested and synthesized.
- Outputs include cooling pipes, analog and digital power, and data lines
- Also need some provision for air flow



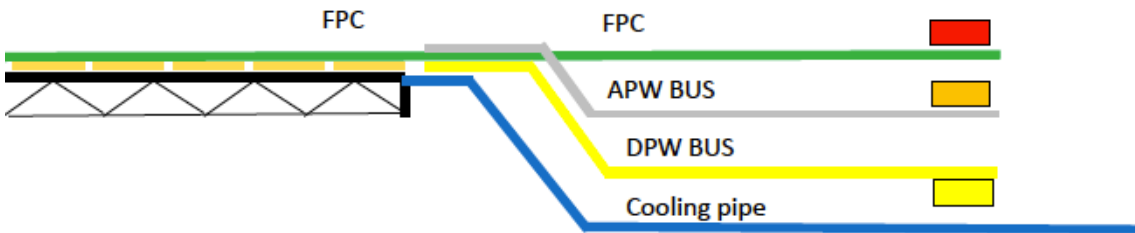
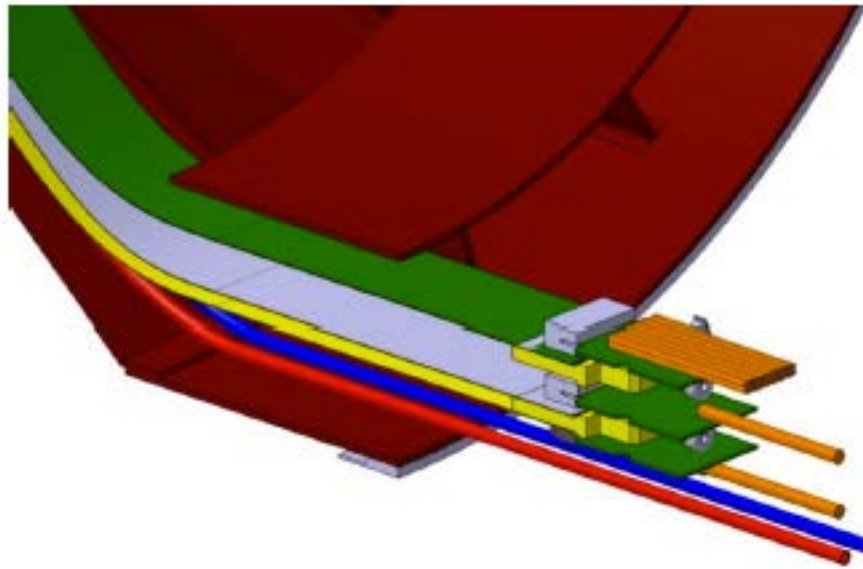
Basic Layout - I



Basic Layout - II



Basic Layout - III



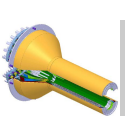
TBV

DATA CONNECTOR = SAMTEC UEC5-XXX-01-X-D-RA-X-T.

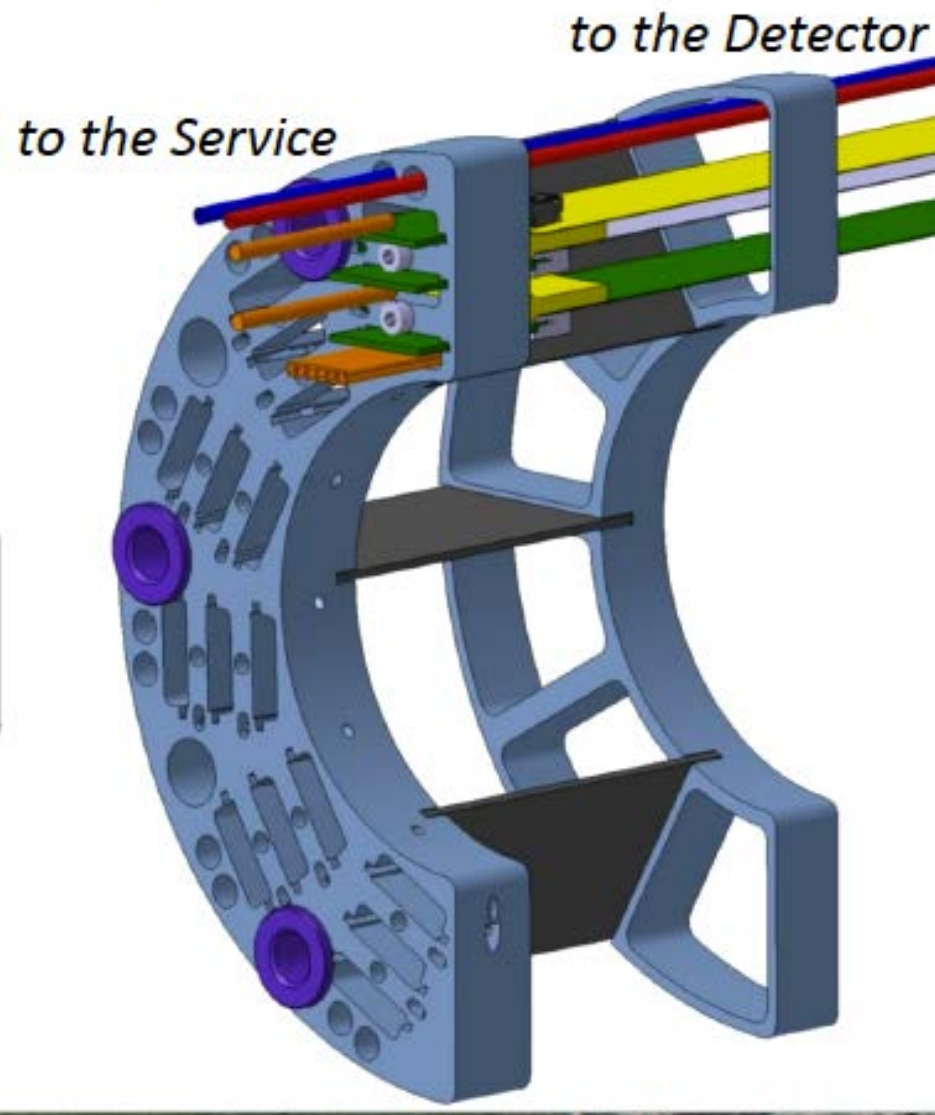
APW CONNECTOR = SAMTEC like custom PCB, custom cable

DPW CONNECTOR = SAMTEC like custom PCB, custom cable

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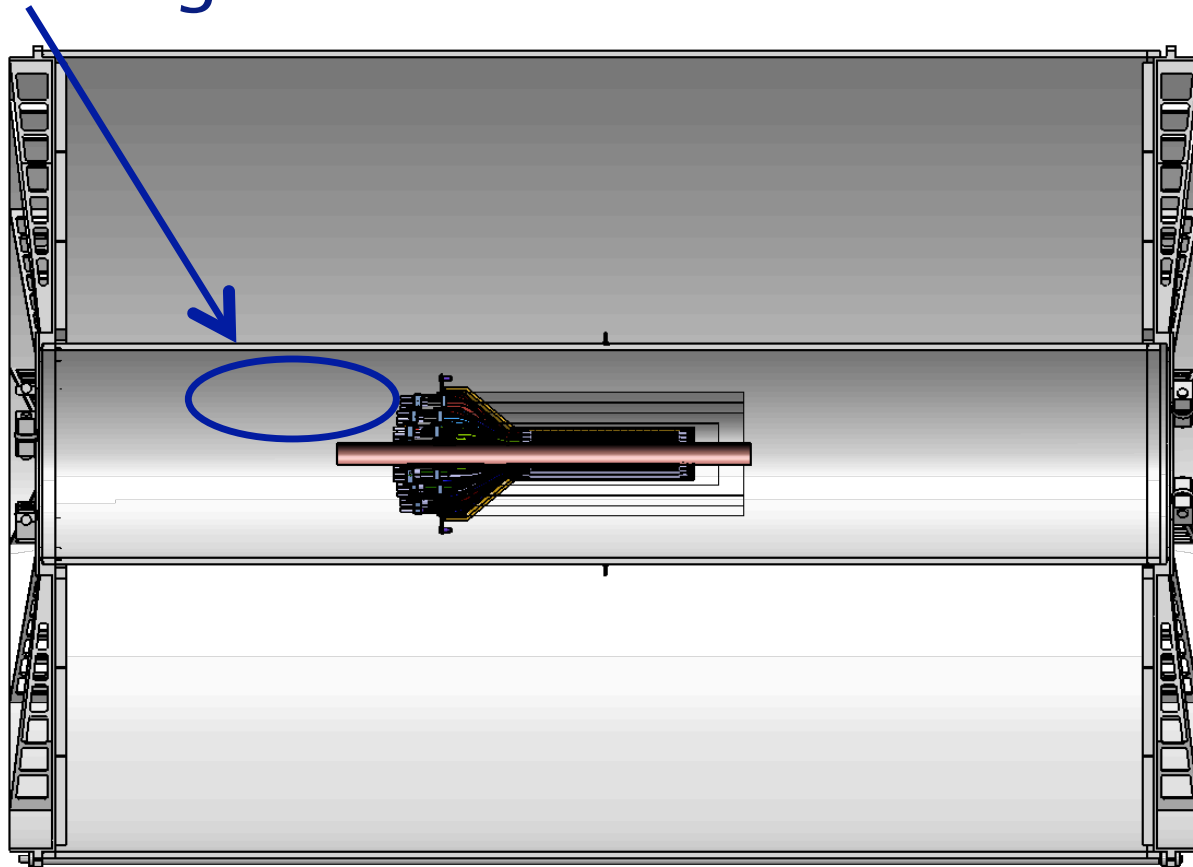


Basic Layout - IV

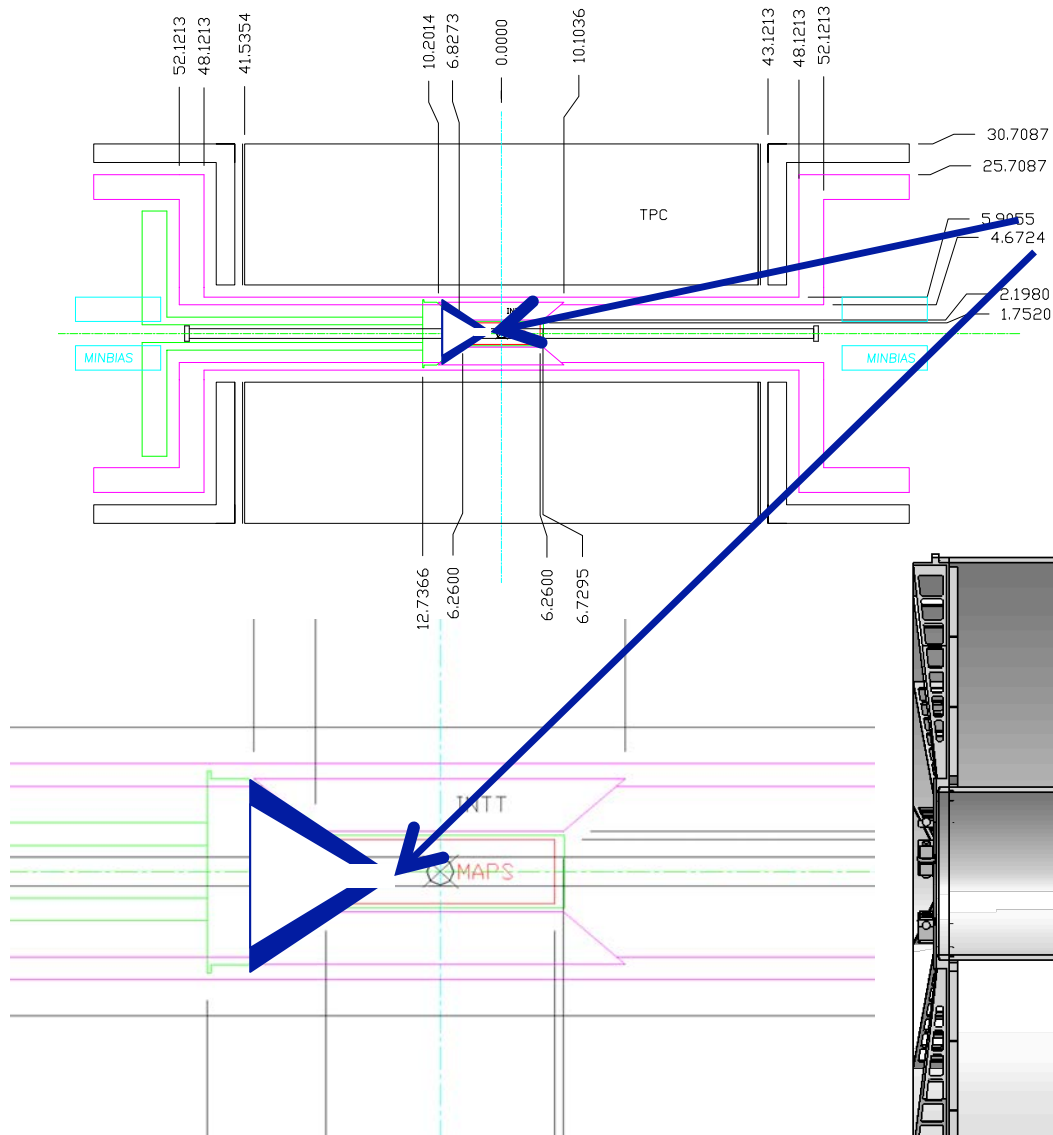


My Biggest Worry - I

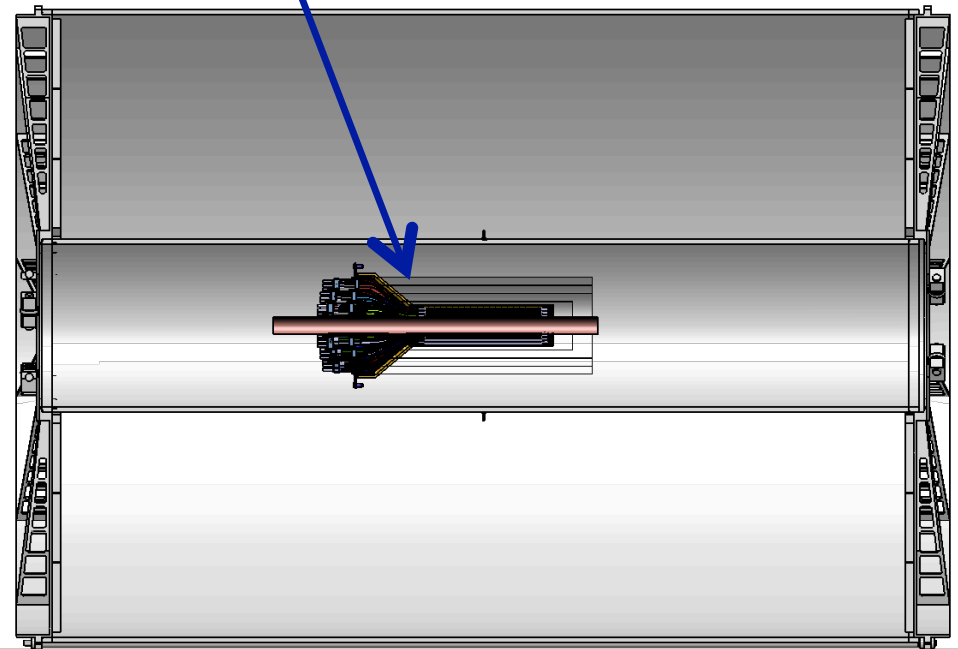
I need to compile all of the information but, based on what I've seen so far, I'm less worried about this region



My Biggest Worry - II



What happens here?



Maybe Not a Worry

What happens here?

